

Applicant : Feng Shi et al.
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Attorney's Docket No.: 13854-065001

REMARKS

Claims 1-3 and 5-23 are currently pending. Claims 21-23 were previously withdrawn from consideration. Reconsideration of the action mailed July 26, 2007, is requested in light of the following remarks.

The Examiner rejected claims 13-16 under 35 U.S.C. § 102(e) as allegedly anticipated by U.S. Patent Publication No. 2003/0011847 ("Dai").

The Examiner has allowed claims 1-3, 5-12, and 17-20. Applicant appreciates the Examiner's allowance of claims 1-3, 5-12, and 17-20.

Section 102 Rejections

Claim 13 stands rejected over Dai. Claim 13 is directed to an EADCM that includes a multi-phase eye quality monitor and an equalizer circuit operable to perform dispersion compensation. The multi-phase eye quality monitor is operable to provide signal distortion measurements of an incoming electrical signal received at the EADCM.

The Examiner states that Dai discloses the claimed EADCM as a combination of feed forward equalizer 13, decision feedback equalizer 14, and eye open X-detect and eye open Y-detect within Dai's transceiver for dispersion compensation. Applicant respectfully disagrees.

Dai discloses the referenced components as separate structures. *See* FIG. 1. Additionally, Dai discloses the eye open X-detect and eye open Y-detect as error detection circuits associated with clock and data recover circuitry 17. *See* FIGS. 1 and 4; paragraphs 56-57 and 64. However, Dai does not disclose these components as being part of a single module (i.e., the EADCM, as required by claim 13. Thus, Dai discloses a structurally distinct device from the claimed EADCM. Additionally, the Examiner does not identify how these separate components are considered an EADCM as a whole when Dai fails to disclose combining the same components into a single module.

Additionally, Dai does not disclose or suggest an EADCM including a multi-phase eye quality monitor that is operable to provide signal distortion measurements of an incoming electrical signal received at the EADCM. In Dai, the error detection circuits including the

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referenced eye open X-detect and eye open Y-detect are associated with the clock and data recover circuitry. See FIGS. 1 and 4; paragraphs 56, and 59-65. The clock and data recover circuitry receives a signal that has already been processed by the feed forward equalizer and the feedback equalizer, which the Examiner has identified as part of Applicants EADCM. See FIG. 1; paragraph 56. These signals are then used for error detection. See paragraph 56. Thus, since output signals from the feed forward equalizer and the feedback equalizer are used for error detection, Dai does not disclose or suggest providing signal distortion measurements of an incoming electrical signal received at the EADCM.

Applicant respectfully submits that claim 13, as well as claims 14-16, which depend from claim 13, are in condition for allowance.

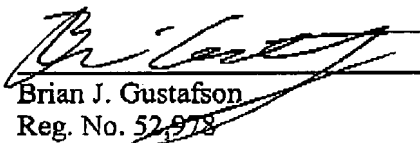
Conclusion

Applicant respectfully requests that all pending claims be allowed.

Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

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